

Zietlow Design - logic analyzer in a day

clock select: 100 MHz
record length: 10 k
acquisition state: halted

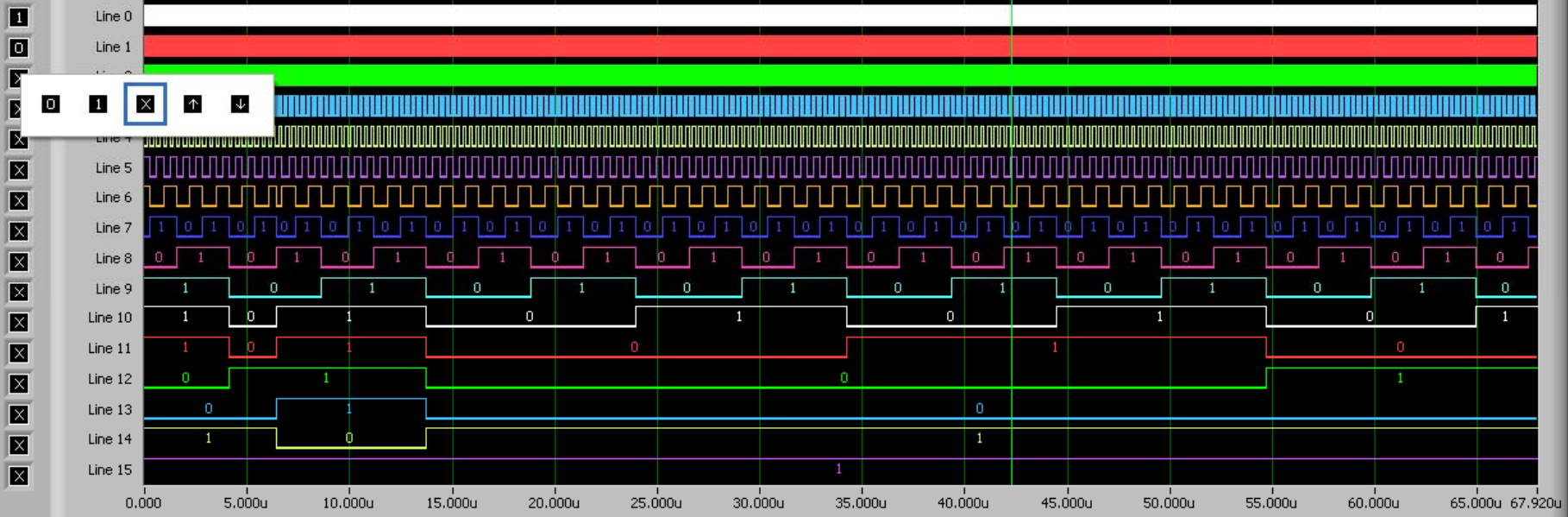
RUN

STOP

pattern generator

trigger position

trigger



pan

zoom

readout logic reads some out of bounds memory
trigger cursor positioning is not working correctly

debug stuff

Line 0		pattern_a: 0	pattern_b: 0	pattern_mask: 0	comp_data: 0	trigger_page: 10	trigger_address: 3	samples: 6792	cursor position: 4.22752µs	EXIT
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